

08 654760

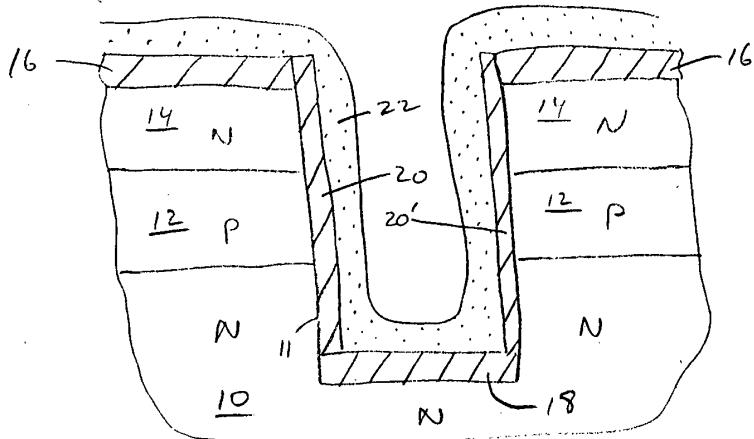


FIG. 1

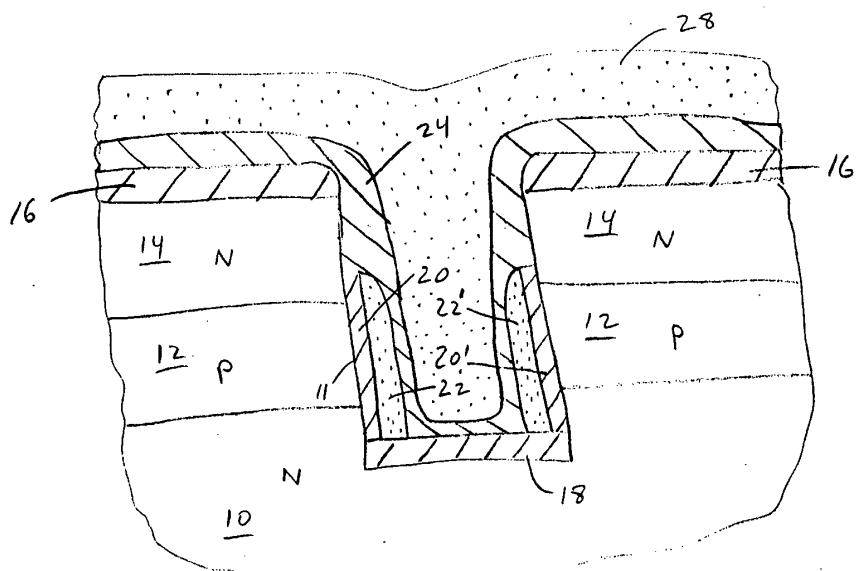


FIG. 2

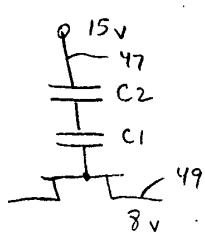


FIG. 3

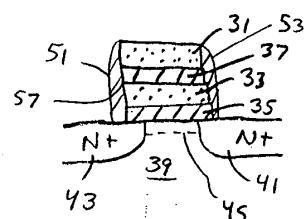


FIG. 4

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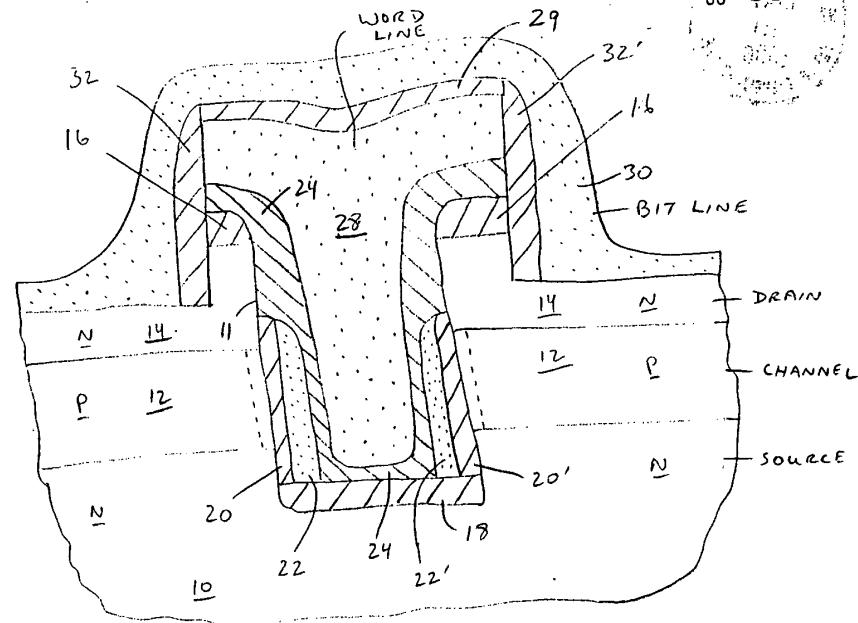


FIG. 5

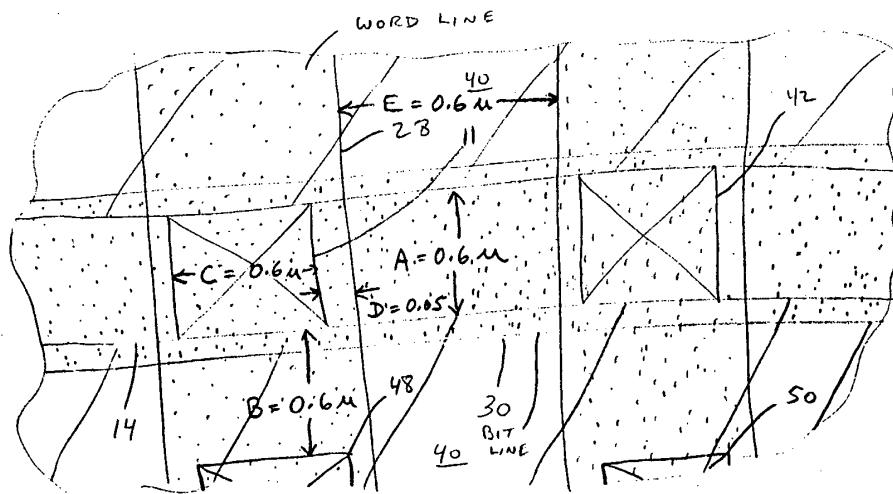


FIG. 6

654760
10
1981
10/10/81
10/10/81

NMOS

P
P-SILICON SUBSTRATE
- 66

FIG. 7A

EEPROM CELL

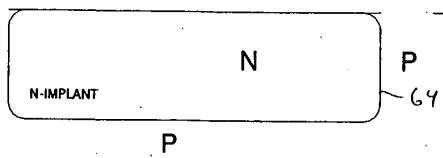


FIG. 7B

PMOS

N

62

FIG. 7C

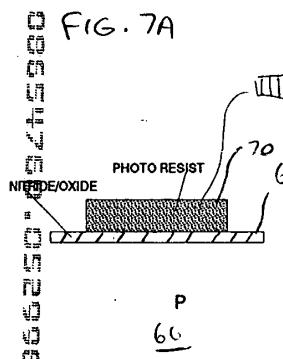


FIG. 8A

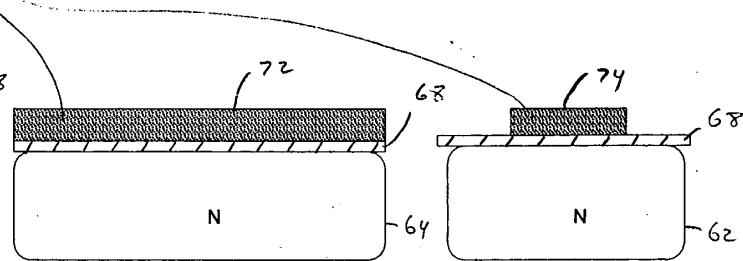


FIG. 8B

FIG. 8C

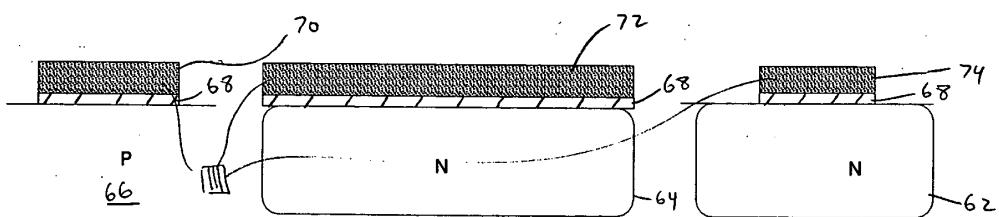


FIG. 9A

FIG. 9B

FIG. 9C

654760
YAK
115
2221
PMOS

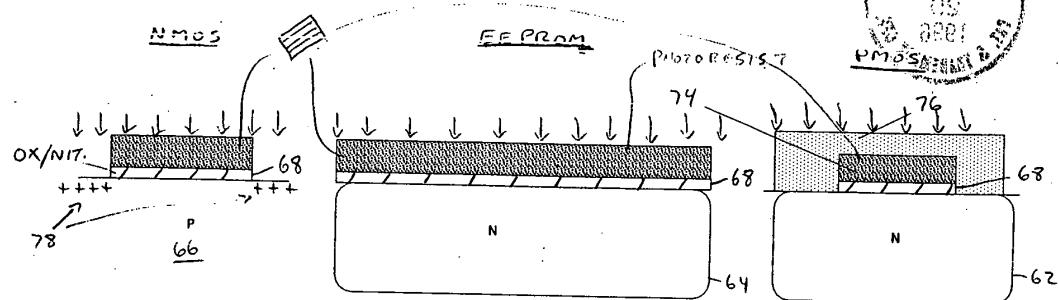


FIG. 10B

FIG. 10A

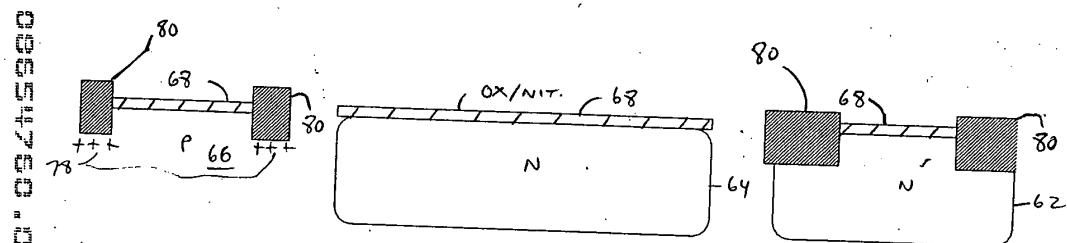


FIG. 10C

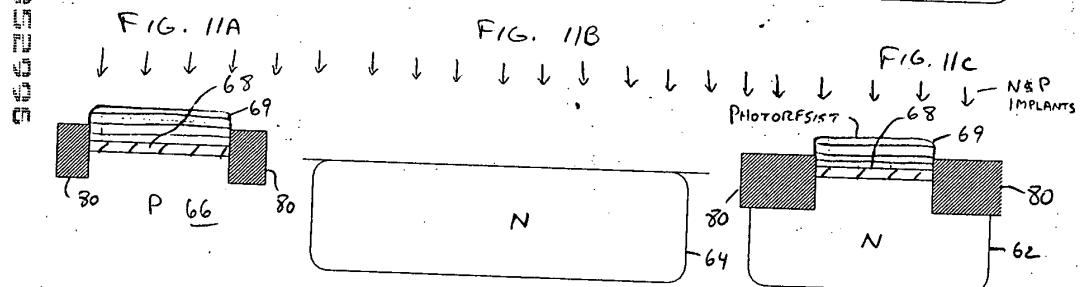


FIG. 11A

FIG. 11B

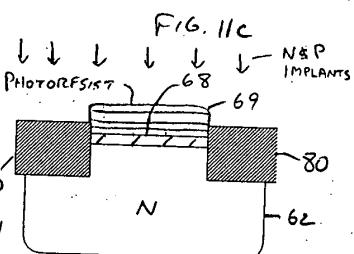


FIG. 12A

FIG. 12B

FIG. 12C

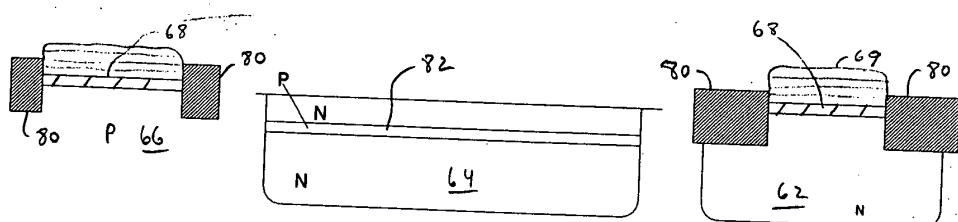


FIG. 13A

FIG. 13B

FIG. 13C

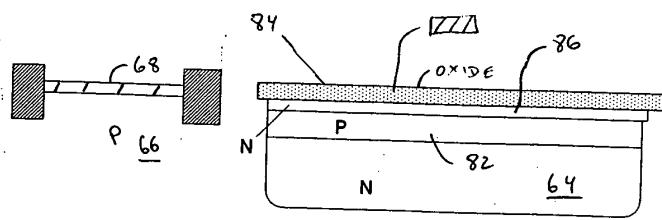


FIG. 14A

FIG. 14B

FIG. 14C

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1991
14

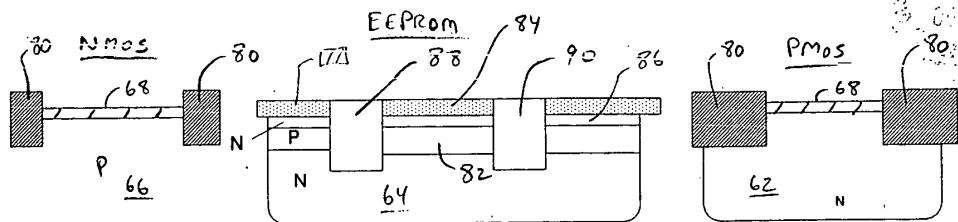


FIG. 15A

FIG. 15B

FIG. 15C

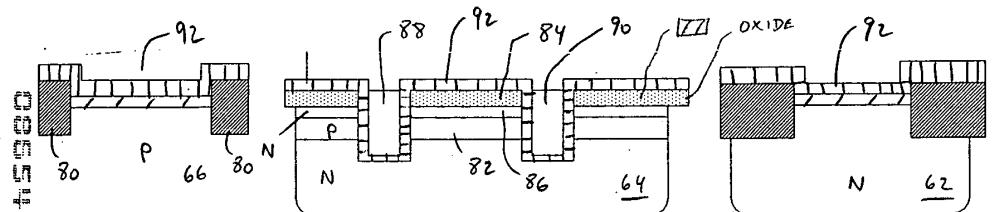


FIG. 16A

FIG. 16B

FIG. 16C

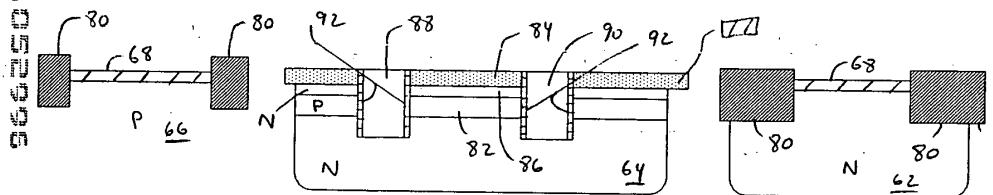


FIG. 17A

FIG. 17B

FIG. 17C

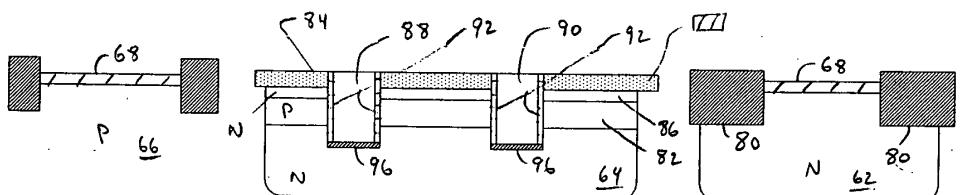


FIG. 18A

FIG. 18B

FIG. 18C

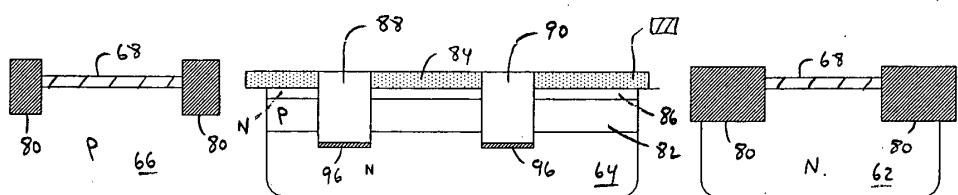


FIG. 19A

FIG. 19B

FIG. 19C

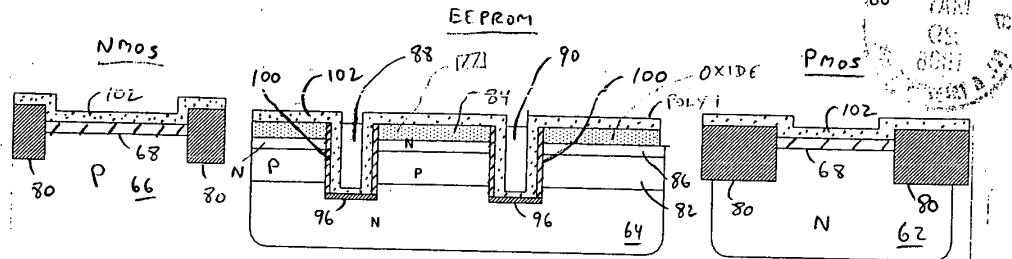


FIG. 20A

FIG. 20B

FIG. 20C

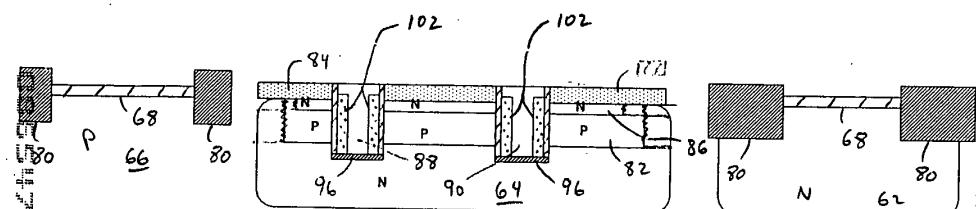


FIG. 21A

FIG. 21B

FIG. 21C

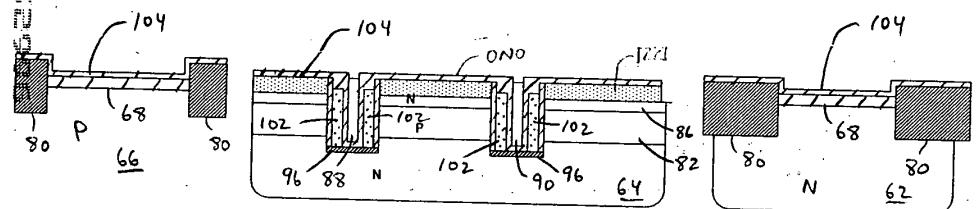


FIG. 22A

FIG. 22B

FIG. 22C

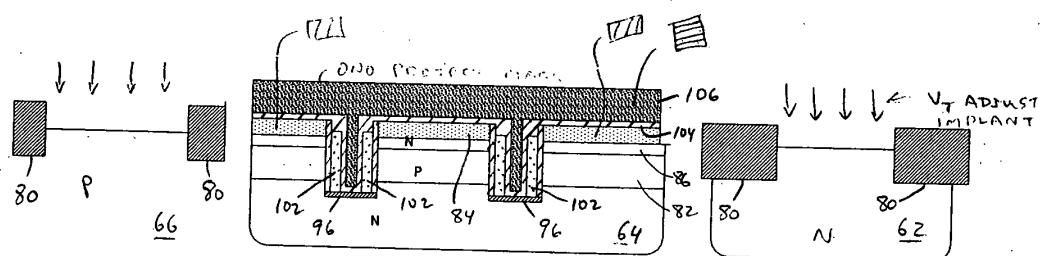


FIG. 23A

FIG. 23B

FIG. 23C

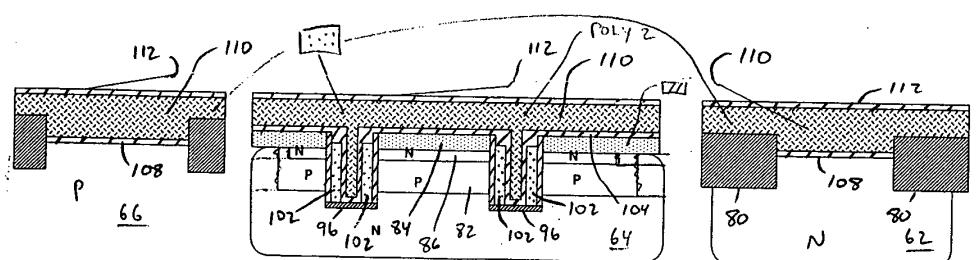


FIG. 24A

FIG. 24B

FIG. 24C

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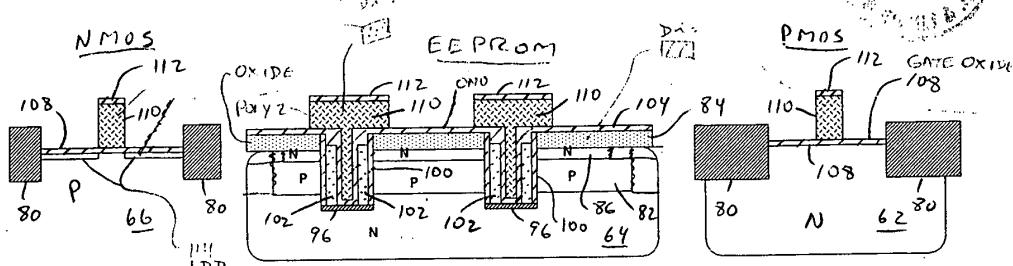


FIG. 25A

FIG. 25B

FIG. 25C

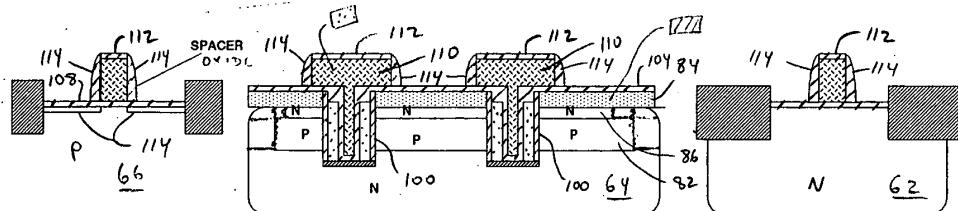


FIG. 26A

FIG. 26B

FIG. 26C

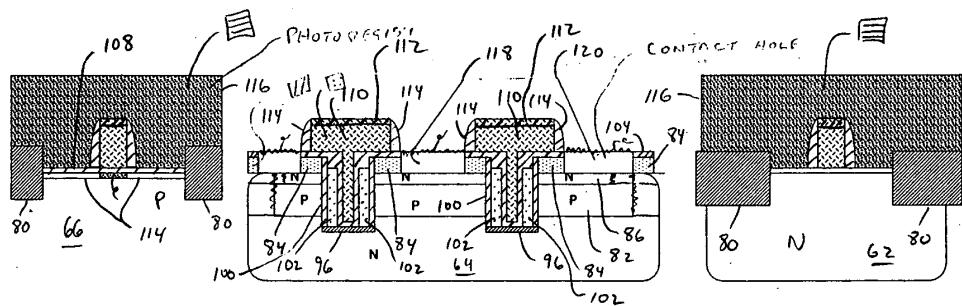


FIG. 27A

FIG. 27B

FIG. 27C

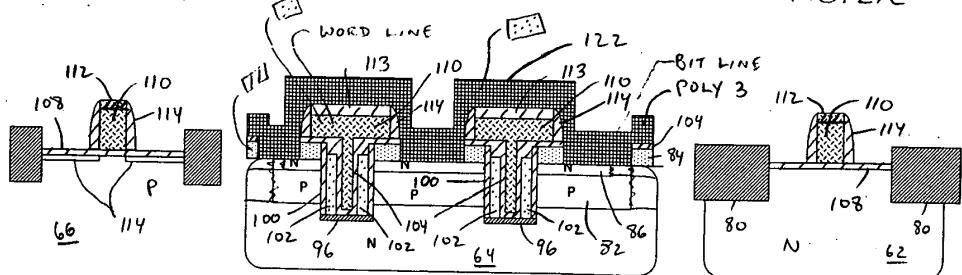


FIG. 28A

FIG. 28B

FIG. 28C

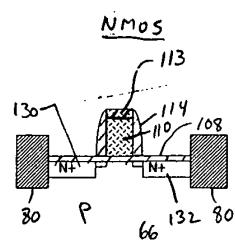


FIG. 29A

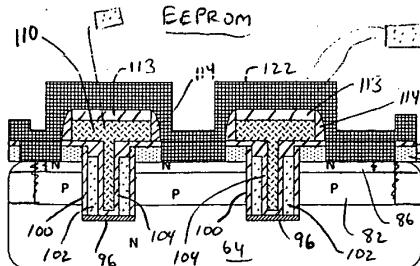


FIG. 29B

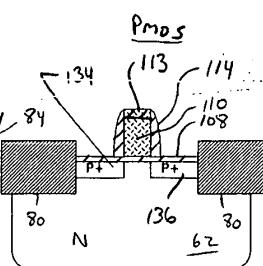


FIG. 29C

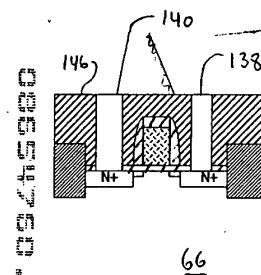


FIG. 30A

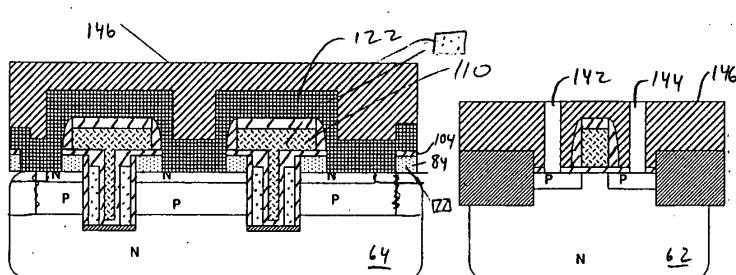


FIG. 30B

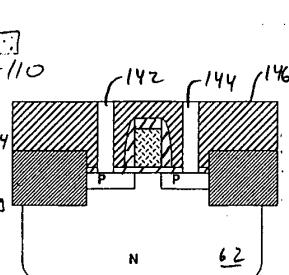


FIG. 30C

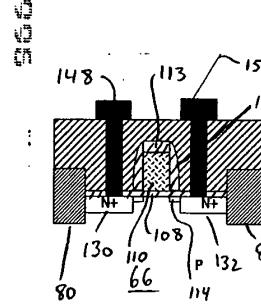


FIG. 31A

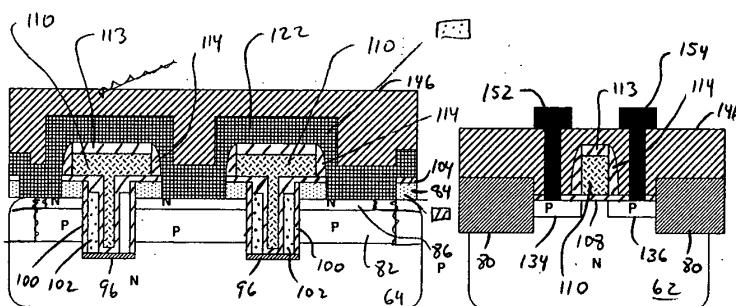


FIG. 31B

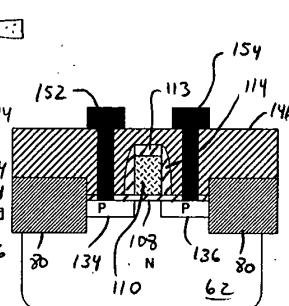


FIG. 31C



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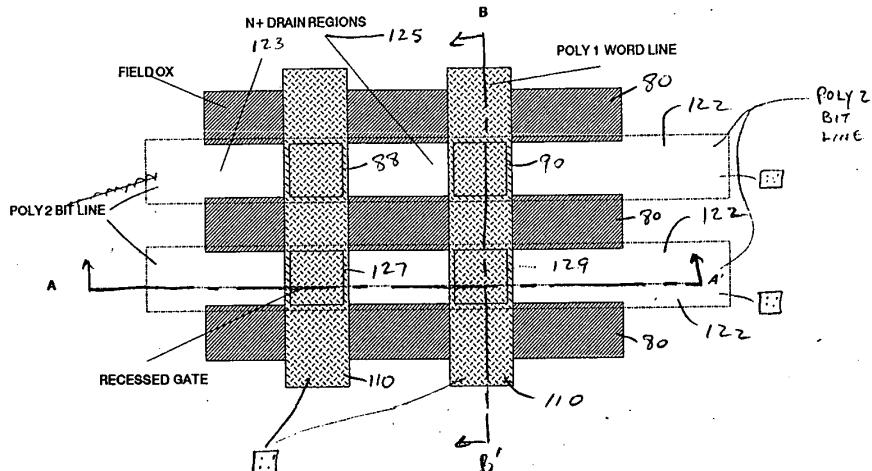


FIG. 32
TOP VIEW-FINISHED CELLS (FOUR)

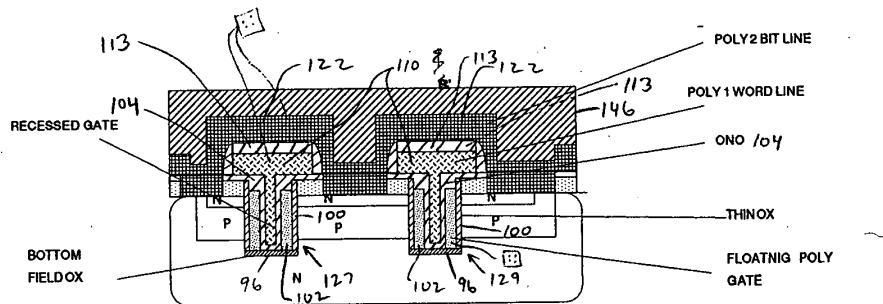


FIG. 33
SECTION A-A'

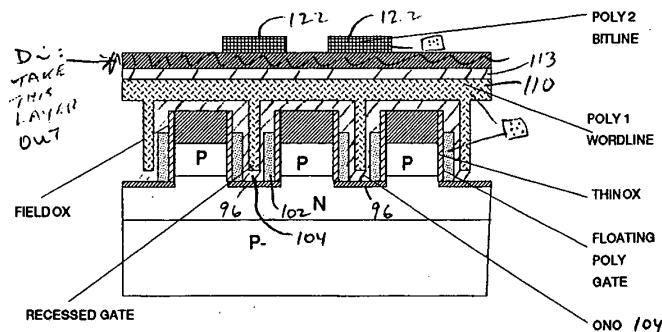


FIG. 34
SECTION B-B'